

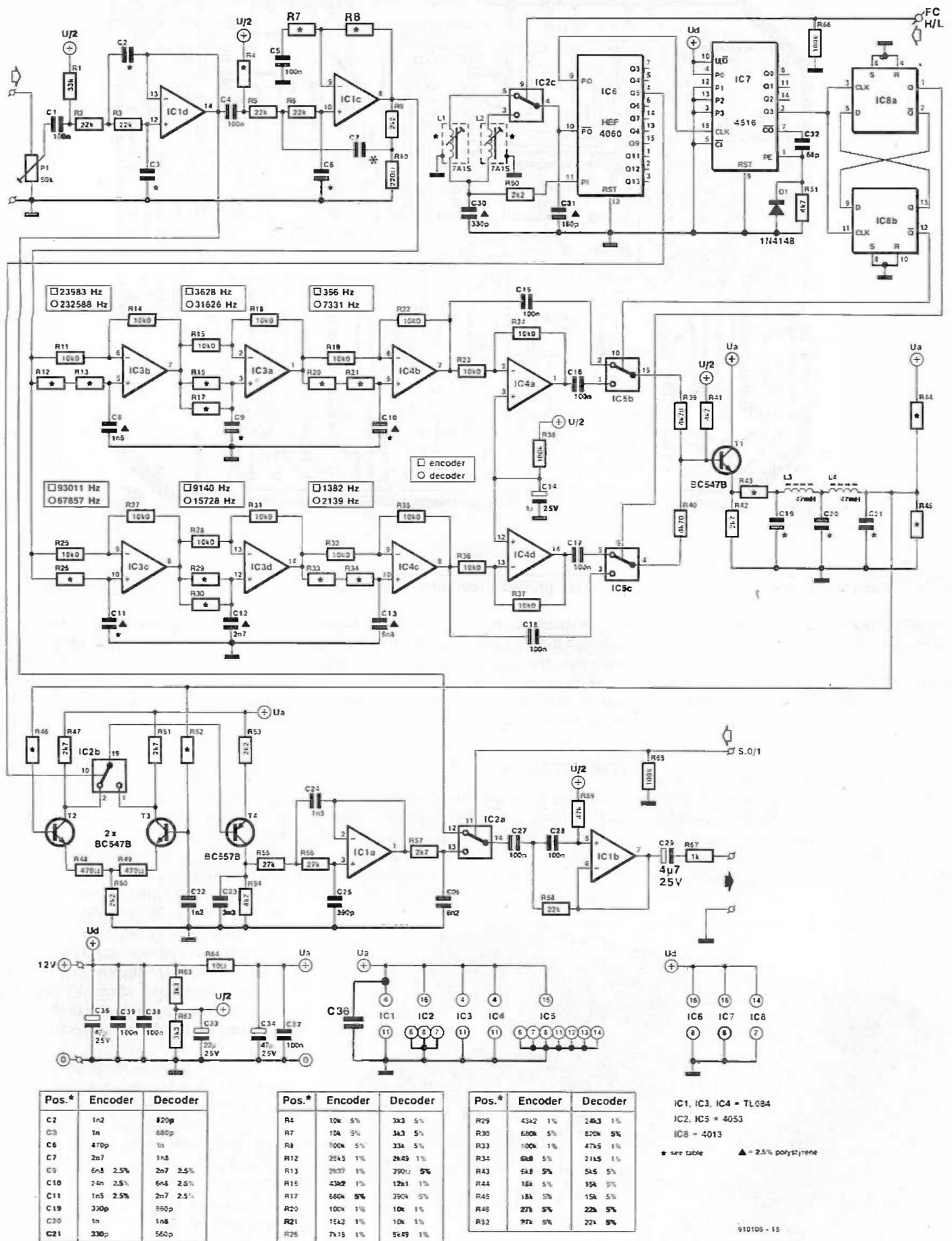


Fig. 5. Circuit diagram of the spectrum shift encoder/decoder.